

Vishay High Power Products

Schottky Rectifier, 1.0 A





PRODUCT SUMMARY			
I _{F(AV)}	1.0 A		
V_{R}	40 V		

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The MBRS140TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	Α		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	380	A		
V _F	1.0 Apk, T _J = 125 °C	1.0 Apk, T _J = 125 °C 0.53			
T _J	Range	Range - 55 to 150			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	MBRS140TRPbF	UNITS	
Maximum DC reverse voltage	V_{R}	40	V	
Maximum working peak reverse voltage	V_{RWM}	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 119 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	380	А
	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	40		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		Α	

MBRS140TRPbF

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.52	0.6	V
		2 A		0.70	0.77	
		1 A	T _J = 125 °C	0.48	0.53	
		2 A		0.63	0.71	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	-	0.1	mA
		T _J = 125 °C		-	4.0	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	80	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		-	2.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar to DO-214AA)	V.	14

Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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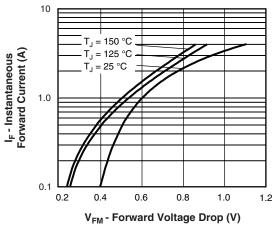


Fig. 1 - Maximum Forward Voltage Drop Characteristics

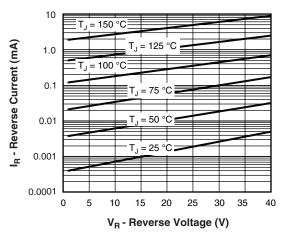


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

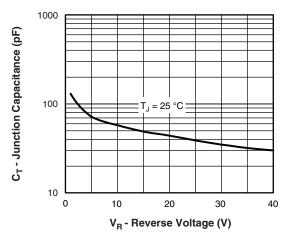


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

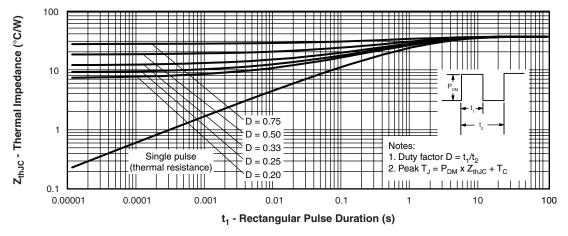


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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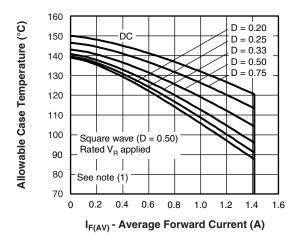


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

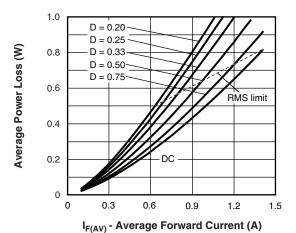


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

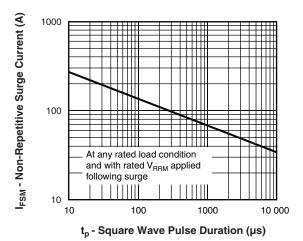


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

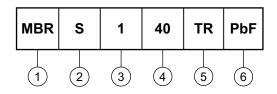
Note



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ORDERING INFORMATION TABLE

Device code



- 1 Schottky MBR series
- 2 S = SMB
- 3 Current rating (1 = 1 A)
- 4 Voltage rating (40 = 40 V)
- TR = Tape and reel (3000 pieces)
- 6 None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95017			
Part marking information	http://www.vishay.com/doc?95029		
Packaging information http://www.vishay.com/doc?95034			
SPICE model	http://www.vishay.com/doc?95299		



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